

FORM PTO-449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
124703SERIAL NO.
10/065,460INFORMATION DISCLOSURE STATEMENT BY APPLICANT
LIST OF ITEMSApplicant
RICHARD J. SAIA, ET ALFiling Date
10/21/02Group
2874

2876

U.S. PATENT AND PUBLICATION DOCUMENTS

| *EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|----------------------|-------------------|--------|------------------|-------|----------|-------------------------------|
| DS | AA US2002/0039464 | 4/4/02 | YOSHIMURA, ET AL | | | |
| | AB | | | | | |
| | AC | | | | | |
| | AD | | | | | |
| | AE | | | | | |

FOREIGN PATENT DOCUMENTS

| | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION YES NO |
|----|--------------------|---------|---------------------------|-------|----------|-----------------------|
| DS | B1 JP2000235127 | 8/29/00 | PATENT ABSTRACTS OF JAPAN | | | |
| | B2 | | | | | |
| | B3 | | | | | |

OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, etc.)

| | | |
|----|----|---|
| DS | C1 | Y. S. LIU, ET AL, "OPTOELECTRONIC PACKAGING AND POLYMER WAVEGUIDES FOR MULTICHIP MODULE AND BOARD-LEVEL OPTICAL INTERCONNECT APPLICATIONS", 1995 IEEE, PP. 185-188. |
| DS | C2 | A COPY OF PCT SEARCH REPORT DATED 5/4/04 IS ENCLOSED. |
| | C3 | |

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant